

Docket No.: 61282-034



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Mitsumi ITO, et al.

Serial No.: 10/634,989

Filed: August 06, 2003

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Group Art Unit:

Examiner:

For: SEMICONDUCTOR DEVICE. METHOD OF GENERATING PATTERN FOR
SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURING
SEMICONDUCTOR DEVICE AND DEVICE FOR GENERATING PATTERN USED
FOR SEMICONDUCTOR DEVICE

PRELIMINARY AMENDMENT

Mail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows: